Fig. 1

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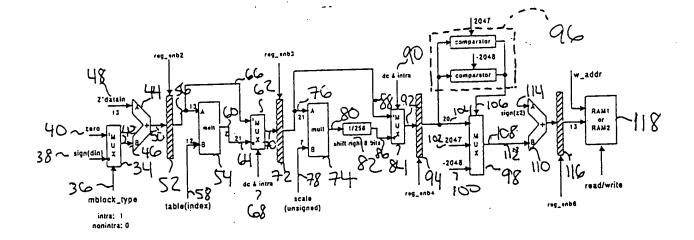


Fig. 2

reset

start

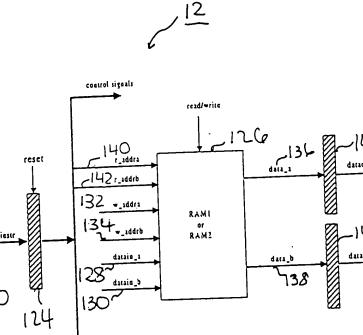
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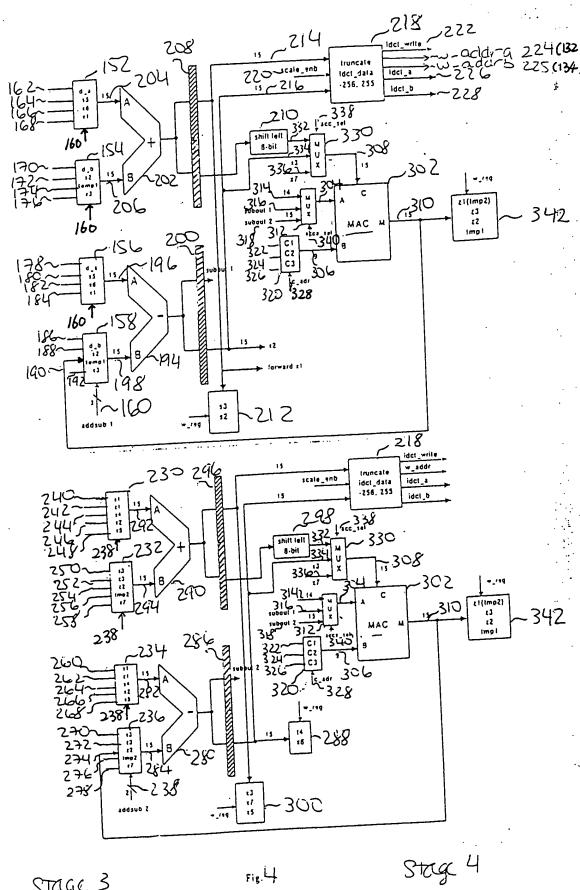


Stage Z

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Fig.3

write back address



Stage 3